	Application No.	Applicant(s)
Notice of Allowability	10/080,911	TURNER, JAMES A.
	Examiner	Art Unit
	Po-Wei (Dennis) Chen	2676
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>27 September 2004</u> .		
2. X The allowed claim(s) is/are <u>1-42 and 44-48</u> .		
3. X The drawings filed on 22 February 2002 are accepted by the Examiner.		
4.		
Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/SB/O Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. Interview Summary Paper No./Mail Dal 8), 7. Examiner's Amendr	te
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DETAILED ACTION

Claims 1-42 and 44-48 are pending in this application. Claims 1, 20, 25, 28 and 34 are independent claims.

The present title of the invention is "Apparatus and Method for Simulating Sensor Imagery".

Allowable Subject Matter

- 1. Claims 1-42 and 44-48 are allowed.
- 2. The following is an examiner's statement of reasons for allowance:

Prior art references do not anticipate or suggest the limitation "an image generator comprising a computer having an output and transmitting thereon a video signal comprising at least two simulation-generated digital data channels... said digital data channels of the video signal from the image generator each comprising a plurality of bit sets each corresponding to a respective location in the field of view and having a preset number of bits of digital data therein; the bit sets of the first channel each representing a respective value of the data variable at a first resolution, and the bit sets of the second channel each representing a respective value of the data variable at a second resolution higher than the first resolution" in combination with the other claim limitations in claim 1.

Prior art references do not anticipate or suggest the limitation "the combiner circuit compares a scaled value from each bit set in the first channel with a scale value from the bit set of the other channel that corresponds to the same location in the field of view and selects based on said comparison a value to be transmitted to the visual display device" in combination with the other claim limitations in claim 20.

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Prior art references do not anticipate or suggest the limitation "said combiner circuit selecting as a selected scaled value for the pixel the scaled value from the first channel if all three scaled values for the pixel are different, selecting as the selected scaled value for the pixel the scaled value of the second channel if the scaled value of the second channel is equal to the scaled value of the first channel but different from scaled value of the third channel, and selecting as the selected scaled value for the pixel the scaled value of the third channel if all the scaled values for the pixel are different" in combination with the other claim limitations in claim 25.

Prior art references do not anticipate or suggest the limitation "scaling the scaled data values to a common scale that allows comparison of values from one channel to values from the other channel, selecting a value from said data values on the common scale based on an assessment of the data being less likely to have been clamped in value by a range of a channel" in combination with the other claim limitations in claim 28.

Prior art references do not anticipate or suggest the limitation "a computerized image generator having an output configured to transmit a video signal having two or more video channels... said first and second data signals each including at least one screen image comprising a number of sets of bits each corresponding to a respective location in said field of view; each set of bits defining a numerical value expressing the data value corresponding to said location in the field of view scaled relative to a respective range of possible data values separated by a respective incremental resolution step value of said range" in combination with the other claim limitations in claim 34.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue

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fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Po-Wei (Dennis) Chen whose telephone number is (703) 305-8365. The examiner can normally be reached on Monday-Thursday from 8:30 AM to 7:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew C Bella can be reached on (703) 308-6829. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3900.

Po-Wei (Dennis) Chen Examiner Art Unit 2676

Po-Wei (Dennis) Chen December 23, 2004

> MATTHEW C. BELLA SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2600

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